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#9/D  
And  
NIE  
J. McMillan  
3/9/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Rinco-Mora      Docket No: TI-33257  
Serial No: 09/977,609      Examiner: Nguyen, Minh  
Filed: 10/15/01      Art Unit: 2816  
For: A SIMPLE FLOATING-GATE MOS THRESHOLD VOLTAGE  
EXTRACTION CIRCUIT & AN ACCURATE LINEAR CAPACITOR-  
MULTIPLIER CIRCUIT

AMENDMENT PURSUANT TO 37 CFR 1.116

Assistant Commissioner for Patents  
Washington, DC 20231

PLEASE DONOT ENTER  
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3/14/03

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)  
I hereby certify that the above correspondence is being  
deposited with the U.S. Postal Service as First Class Mail  
in an envelope addressed to: Assistant Commissioner for  
Patents, Washington, DC 20231 on 2-17-03  
*Tommie Chambers*  
Tommie Chambers

Dear Sir:

03/06/2003 JMC/HLLA 00000004 200668 09977609

01 FC:1201 420.00 CH

Responsive to the Office Action mailed November 19, 2002 in connection with  
the above identified application, Applicant respectfully submits the following  
amendments and remarks.

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MAR-3 2003  
J/C 2800 MAIL ROOM  
TECHNOLOGY CENTER 2800  
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